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DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L40 I2 and L39

30

L40

DB=PGPB,USPT; PLUR=YES; OP=OR

L39 (703/13-28)[CCLS]

6126

L39

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L38</u>	L33 and I11	49	<u>L38</u>
<u>L37</u>	L33 and I10	31	<u>L37</u>
<u>L36</u>	L33 and I9	184	<u>L36</u>
<u>L35</u>	L33 and I8	16	<u>L35</u>
<u>L34</u>	L33 and I7	21	<u>L34</u>
<u>L33</u>	L32 and (compil\$5 or evaluat\$5 or test\$4 or scan\$5)	3782	<u>L33</u>
<u>L32</u>	short\$4 near5 circuit\$4 near45 (logic or "AND" or "OR" or "NAND" or "NOR" or "XOR", or conju\$7 or disjunct\$5)	10305	<u>L32</u>

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

<u>L31</u>	(boolean and (logic or conjunct\$5 or disjunct\$5) and evaluat\$6 and short\$3 near4 circuit\$4).clm.	4	<u>L31</u>
<u>L30</u>	I28 and boolean	1	<u>L30</u>
<u>L29</u>	I27 and I1L28	0	<u>L29</u>
<u>L28</u>	(koch near2 kenneth near2 elmon).in.	3	<u>L28</u>

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<u>L27</u>	I2 and I11	38	<u>L27</u>
<u>L26</u>	I2 and I10	8	<u>L26</u>
<u>L25</u>	I2 and I9	35	<u>L25</u>
<u>L24</u>	I2 and I8	9	<u>L24</u>
<u>L23</u>	I2 and I7	18	<u>L23</u>
<u>L22</u>	I12 and I11	15	<u>L22</u>
<u>L21</u>	I12 and I10	0	<u>L21</u>
<u>L20</u>	I12 and I9	5	<u>L20</u>
<u>L19</u>	I12 and I8	3	<u>L19</u>
<u>L18</u>	I12 and I7	9	<u>L18</u>
<u>L17</u>	I6 and I11	15	<u>L17</u>
<u>L16</u>	I6 and I10	0	<u>L16</u>
<u>L15</u>	I6 and I9	8	<u>L15</u>
<u>L14</u>	I6 and I8	4	<u>L14</u>
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<u>L12</u>	I6 and (compil\$5 or debug\$5)	327	<u>L12</u>

DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L11</u>	(716/16-18)[CCLS]	2433	<u>L11</u>
<u>L10</u>	(326/41,114,125)![CCLS]	1916	<u>L10</u>
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<u>L8</u>	(712/201-228, 245-248)[CCLS]	7556	<u>L8</u>
<u>L7</u>	(712/2-300)[CCLS]	13884	<u>L7</u>

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<u>L6</u>	L5 and (program\$6 or instruction\$1 or command\$3)	339	<u>L6</u>
<u>L5</u>	L4 and (prior or recent or last or result\$3 or outcome or feedback or feed\$3 near3 back or feedback)	339	<u>L5</u>
<u>L4</u>	L3 and address\$5	339	<u>L4</u>
<u>L3</u>	L2 and default\$1	379	<u>L3</u>
<u>L2</u>	L1 and short\$4 near5 circuit\$4	649	<u>L2</u>
<u>L1</u>	boolean and (logic or "AND" or "OR" or "NAND" or "NOR" or "XOR", or conju\$7 or disjunct\$5) near18 (evaluat\$5 or test\$5)	5819	<u>L1</u>

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Citation



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» Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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1. Upper and Lower Bounds on the Number of Disjunctive Forms

Tatsumi, H.; Miyakawa, M.; Mukaidono, M.;

[Multiple-Valued Logic, 2006. ISMVL 2006. 36th International Symposium on](#)

17-20 May 2006 Page(s):8 - 8

Digital Object Identifier 10.1109/ISMVL.2006.44

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2. Comparative study of strategies for formal verification of high-level processors

Velev, M.N.;

[Computer Design: VLSI in Computers and Processors, 2004. ICCD 2004. Proceedings. IEEE International](#)

11-13 Oct. 2004 Page(s):119 - 124

Digital Object Identifier 10.1109/ICCD.2004.1347910

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3. On functions defined on free Boolean algebras

Rosenberg, N.; Simovici, D.A.; Jaroszewicz, S.;

[Multiple-Valued Logic, 2002. ISMVL 2002. Proceedings 32nd IEEE International Symposium on](#)

15-18 May 2002 Page(s):192 - 199

Digital Object Identifier 10.1109/ISMVL.2002.1011089

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4. An evaluation the uncertainty of textual data with logic and statistics

Larouk, O.; Batache, M.;

[Proceedings of ISUMA - NAFIPS '95, The Third International Symposium on Uncertainty Modeling and](#)[Annual Conference of the North American Fuzzy Information Processing Society](#)

17-20 Sept. 1995 Page(s):739 - 744

Digital Object Identifier 10.1109/ISUMA.1995.527787

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